Solar_{py} Energy

"Solar Park" 31 MW farm in Les Mees/France 6 solar PV plants (Eco Delta Développement, EDD)

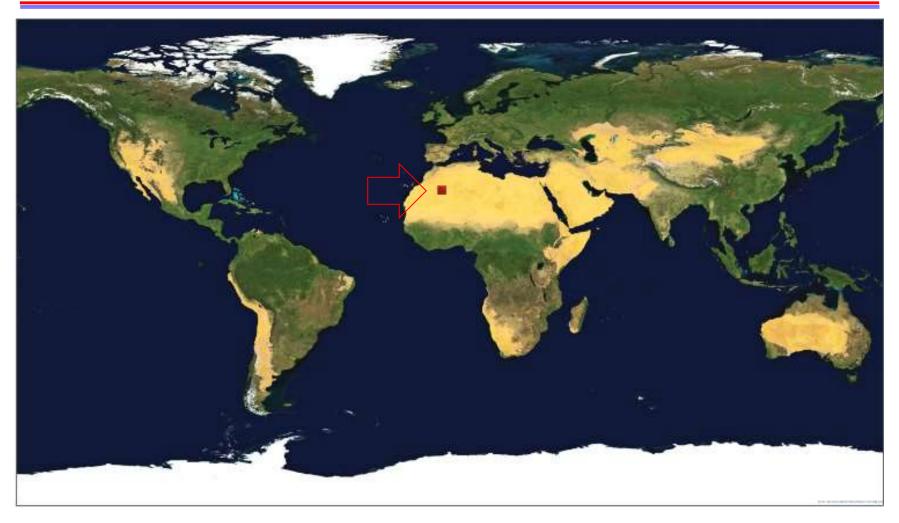
112,000 solar modules on 70 hectares. Inverters: low- and medium-voltage components/ transformers. Siemens responsible for the civil works and substructure, performs maintenance on power plants.

Agenda

- Intro
- Solar insolation, power density, solar emission spectrum
- Utility size(solar farms) & residential PV arrays
- Principle of silicon solar photo-voltaic (PV), technology
 - Semiconductor band structure, gap, junctions
 - Charge carriers in n-type and p-type semiconductors
 - Photocell operation, efficiency
 - Silicon wafer, cell manufacture
 - Materials and emissions in construction
- US installations and performance, system cost and incentives
- Solar power strategic issues

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Partially Sunny World

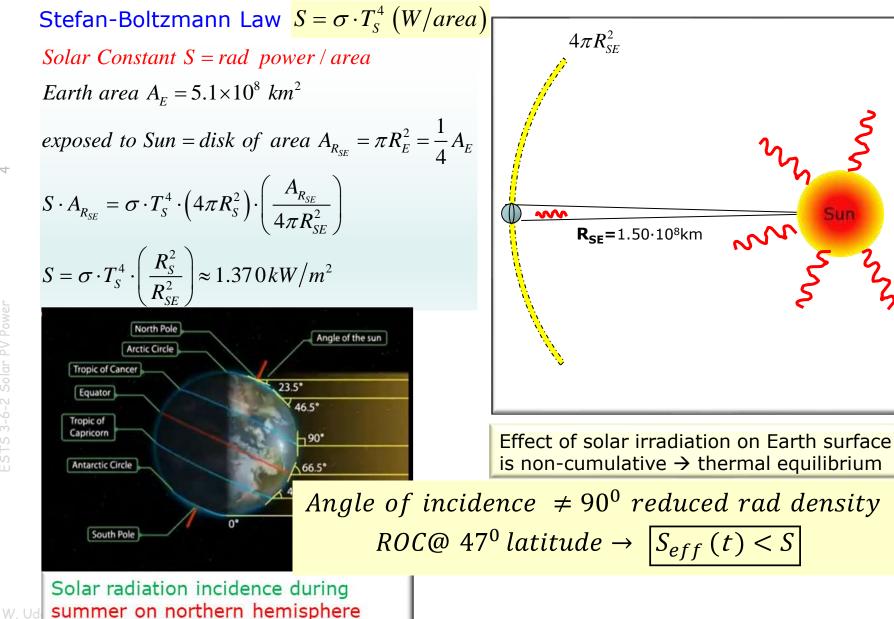


Demand 20 TW worldwide → harvest sunlight (total insolation 10⁵ TW). Methods:
→Direct (photon → e⁻, h exciton) conversion to el. :photo-voltaic (PV), amorphous Si, single crystal Si, thin-film, organic solar cells
> Indicate (the enclosed enc

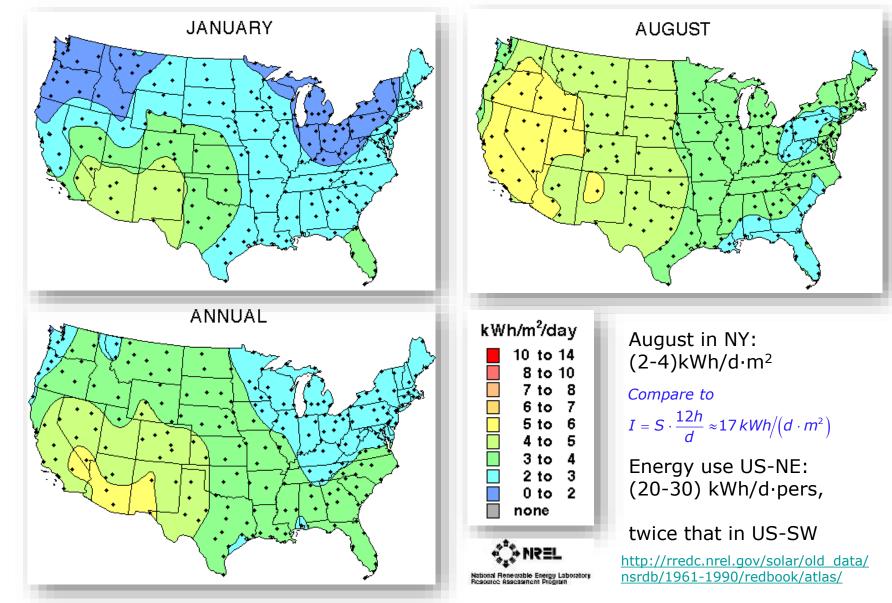
 \rightarrow Indirect (thermal) conversion to electricity: concentrated solar power plant (CSP)

 $^{\circ}$

Free Power: Solar Radiation

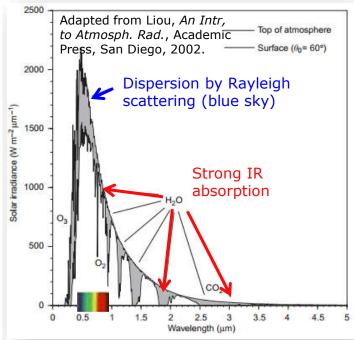


Average Daily Insolation



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Selective Filter Effect of Atmosphere



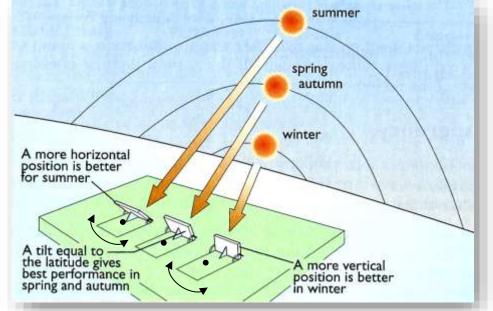
Apparent orbits of Sun on sky require different tilt of sensors for best efficiency ε . Can vary by $\Delta \varepsilon = \pm 15\%$.

Concentrated Solar Power= CSP "Receivers" collect and focus sunlight, Tracking for (θ, ϕ) best efficiency. Intense solar radiation available at wave lengths from the UV, over visible to IR. Spectral gaps due to atmospheric absorption.

Utilization for electricity production:

Photo-voltaic (PV) \rightarrow direct electrical conversion. Concentrated Solar (CSP)= thermal conversion

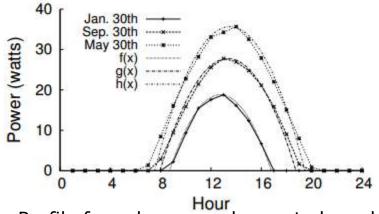




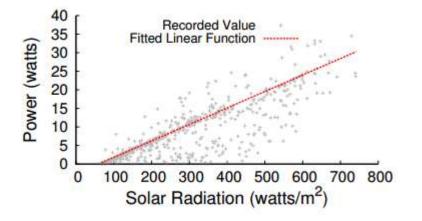
Adapted from G. Boyle et al., Renewable energy, OUP

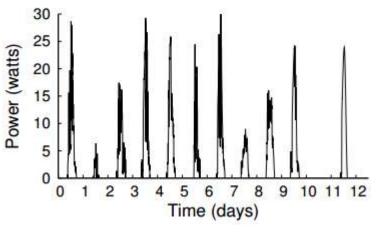
Task: Transform solar radiation ($\hbar \varpi$ photons) to electricity (free electrons)

Actual Cell Field Performance (Expt. MIT)



Profile for solar power harvested on clear and sunny days in January, May, and September, and the quadratic fit functions





Power generated during a 12-day period in October 2009 from Kyocera solar panel, maximum power output 65 watts at 17.4 volts under full sunlight.

Relationship between the solar radiation nearby weather station observes and the power generated by solar panel.

Navin Sharma, Jeremy Gummeson, David Irwin, and Prashant Shenoy University of Massachusetts, Amherst

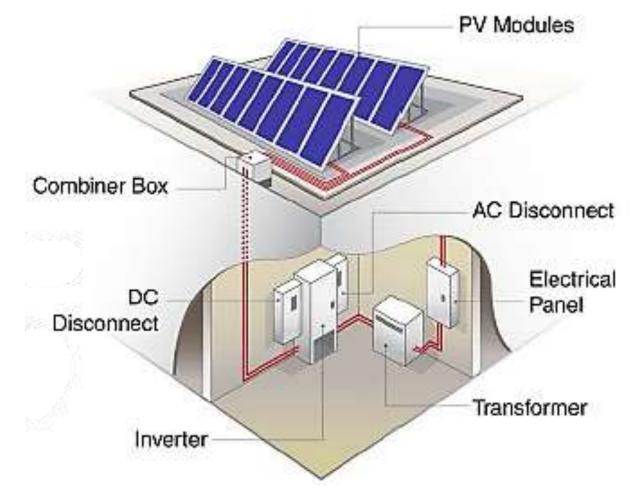
Residential/Commercial Installations



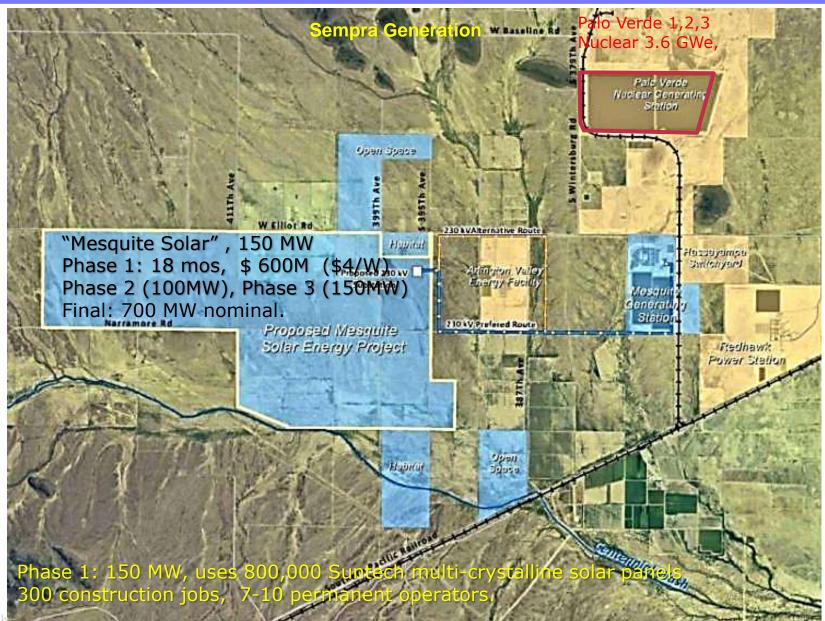
Solar thermal installation at the Allison Inn, Oregon

2022: 5.9 GW_{dc} = nearly 700,000 systems installed (1,687 MW_{dc} in Q4), up 40% from 2021. Supply chain problems: China, tariffs

Residential/Commercial PV Installation



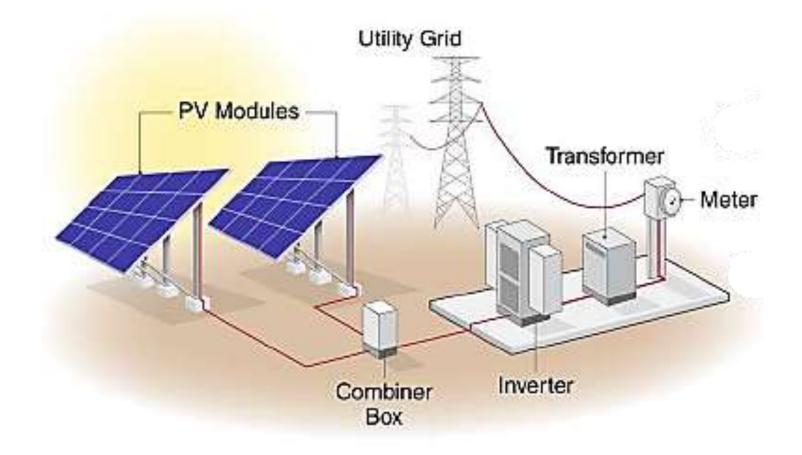
Big Commercial US PV Projects: Mesquite Solar



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W. Udo Sc

Utility PV Installation



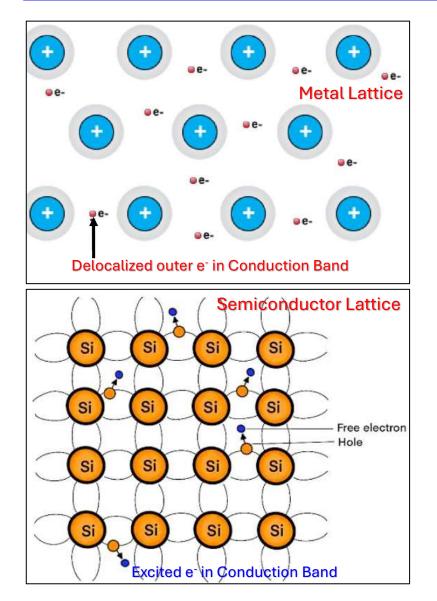
Agenda

• Intro

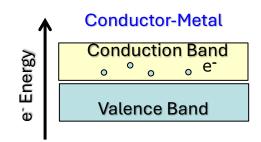
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Task: Transform solar radiation ($\hbar \varpi$ photons) to electricity (free electrons). Find material that is el. neutral in "darkness," and electrically polarized in light

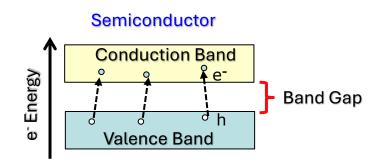
Charge Carriers in Metals and Semiconductors



Many body (~10²³) electrons have overlapping energy states \rightarrow 2 energy bands: bound valence e⁻ form valence band. Free electrons move in conduction band.



Ground State: Filled valence band + free electrons in conduction band (=Fermi gas).

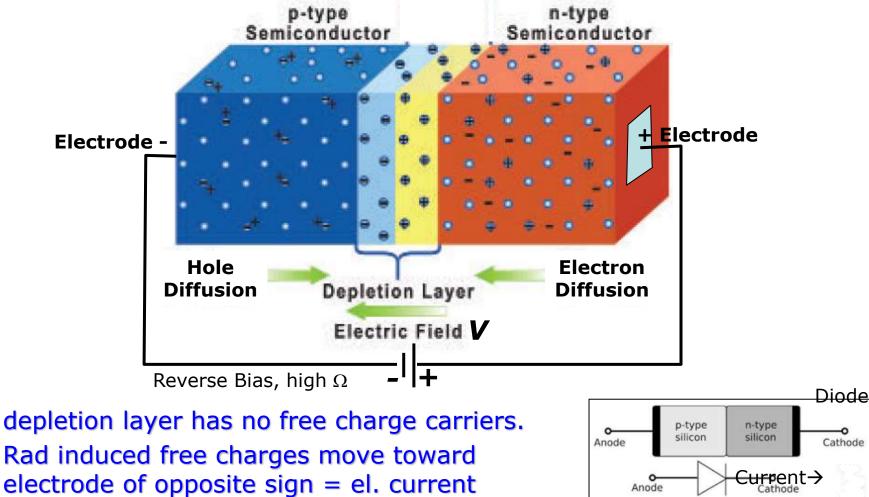


Ground State: No (few) free charge carriers. Excitation produces (quasi-) free e-hole pairs (excitons).

Semiconductor pn Junction (Diode)

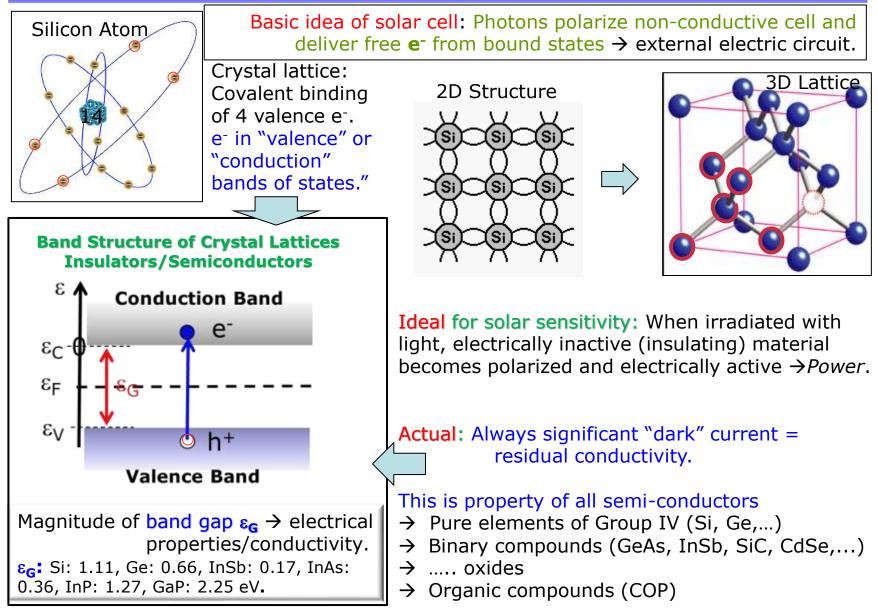
N-Type= Si doped with phosphorous emits $e^- \rightarrow$ promotes free e^-

- P-Type = Si doped with Boron captures e-, promotes free h^+ .
- → At normal *T*: both e^{-} and h^{+} diffuse → annihilate in a depletion layer.



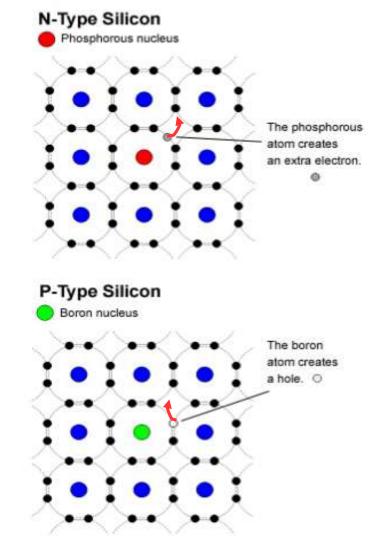
W. Udo Schröder, 202

PV Basics: Properties of Semiconductors



"Purification:" Silicon Doping

Desired properties \rightarrow super-pure "intrinsic" semi-conductor materials. Not achievable directly \rightarrow always slightly conducting! \rightarrow compensate impurities by "doping" crystal.



P (phosphorus, Group V element) has one valence electron more than Si, which is less bound and moves in the Si-conduction band.

P-doped Si= **N-type silicon**

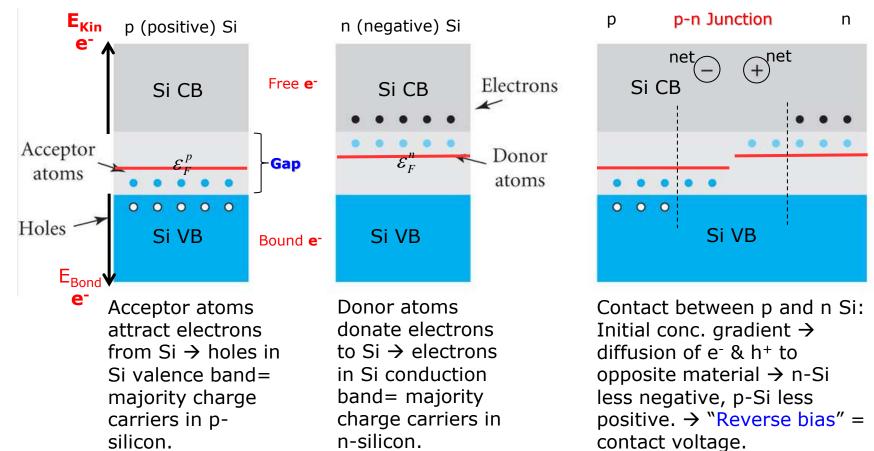
P is an electron donor.

B (boron, Group III element) has one electron less than Si, creates a hole in the Si-valence band, which can move in that band.

B-doped Si = *P*-type silicon, *B* is an electron *acceptor*.

Doped Semiconductor/Junction

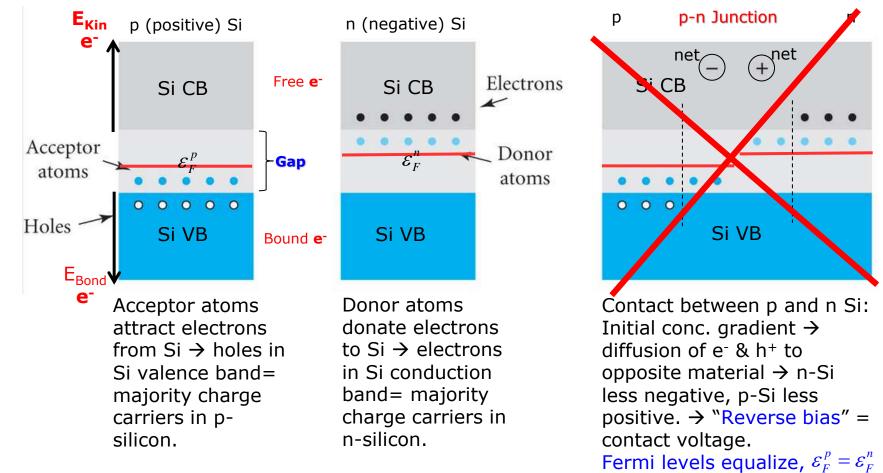
Intrinsic Si: $N_{si} = 5 \times 10^{22}$ atoms/cm³. Doping concentration $N_{dop} = (10^{13} - 10^{18})$ cm⁻³ « N_{si} . Retains mainly Si band structure (VB & CB). Doping atoms have different electronic levels \rightarrow influences Fermi Level ϵ_{F} (half-way between fully occupied & completely empty)



Fermi levels equalize, $\mathcal{E}_F^p = \mathcal{E}_F^n$

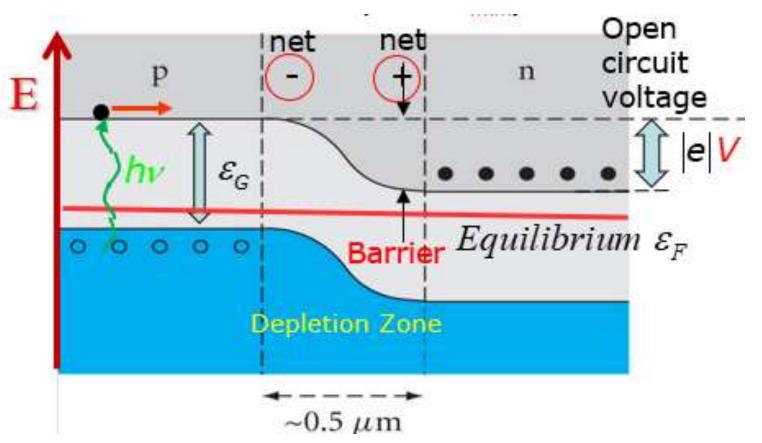
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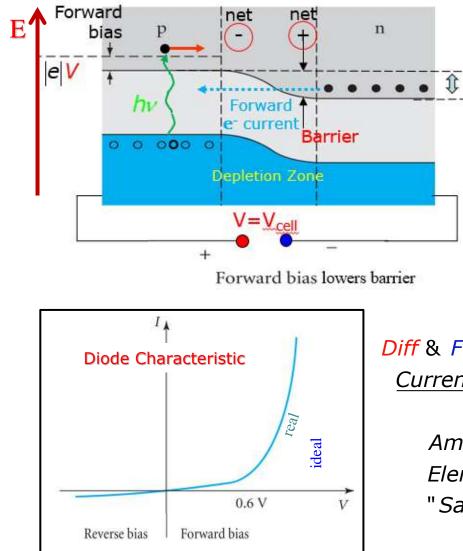


Semiconductor Junction Diodes

In asymmetric junction zone, no free charge carriers (e⁻ or holes) \rightarrow Depletion Zone. Extra + charges in n-region \rightarrow e- have lower energy (more strongly bound). Effective barrier prevents additional migration of charges \rightarrow chargefree zone (except thermal excitations & photo h_V)!



Semiconductor Junction Diodes



Forward: normal $n \rightarrow p$ across junction due to thermal \rightarrow No Bias applied $\rightarrow I \approx 0$ Forward bias lowers barrier $\rightarrow I \neq 0$, increases.

Reverse bias: smaller $p \rightarrow n$ reverse e⁻ current due to thermal transitions over higher barrier. \rightarrow *Diode Characteristic*

 $\frac{\text{Diff \& Field}}{\underline{Currents}}: I(V) = I_{sat} \cdot \left\{ e^{+e \cdot V/kT} - 1 \right\} \quad \begin{array}{c} \text{Fermi - Dirac} \\ \text{Statistics} \end{array}$

Ambient T : kT = 25 m eV @ T = 293KElementary charge e, "Saturation"("dark", "field") current I_{sat}

Cell Equivalent Electronic Circuits

